

FIGURE 1 - Standard Network Processor Configuration (Prior Art)

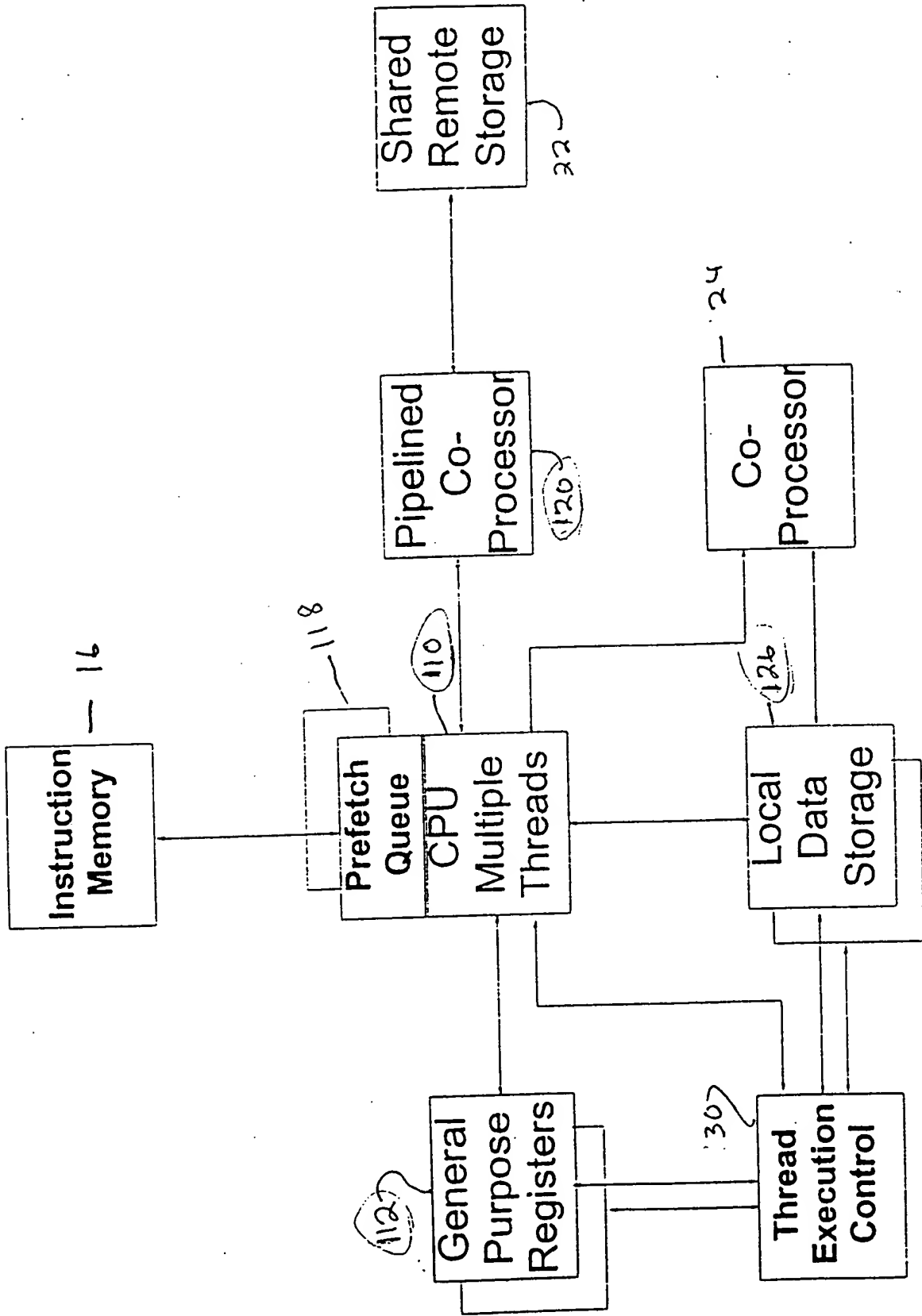
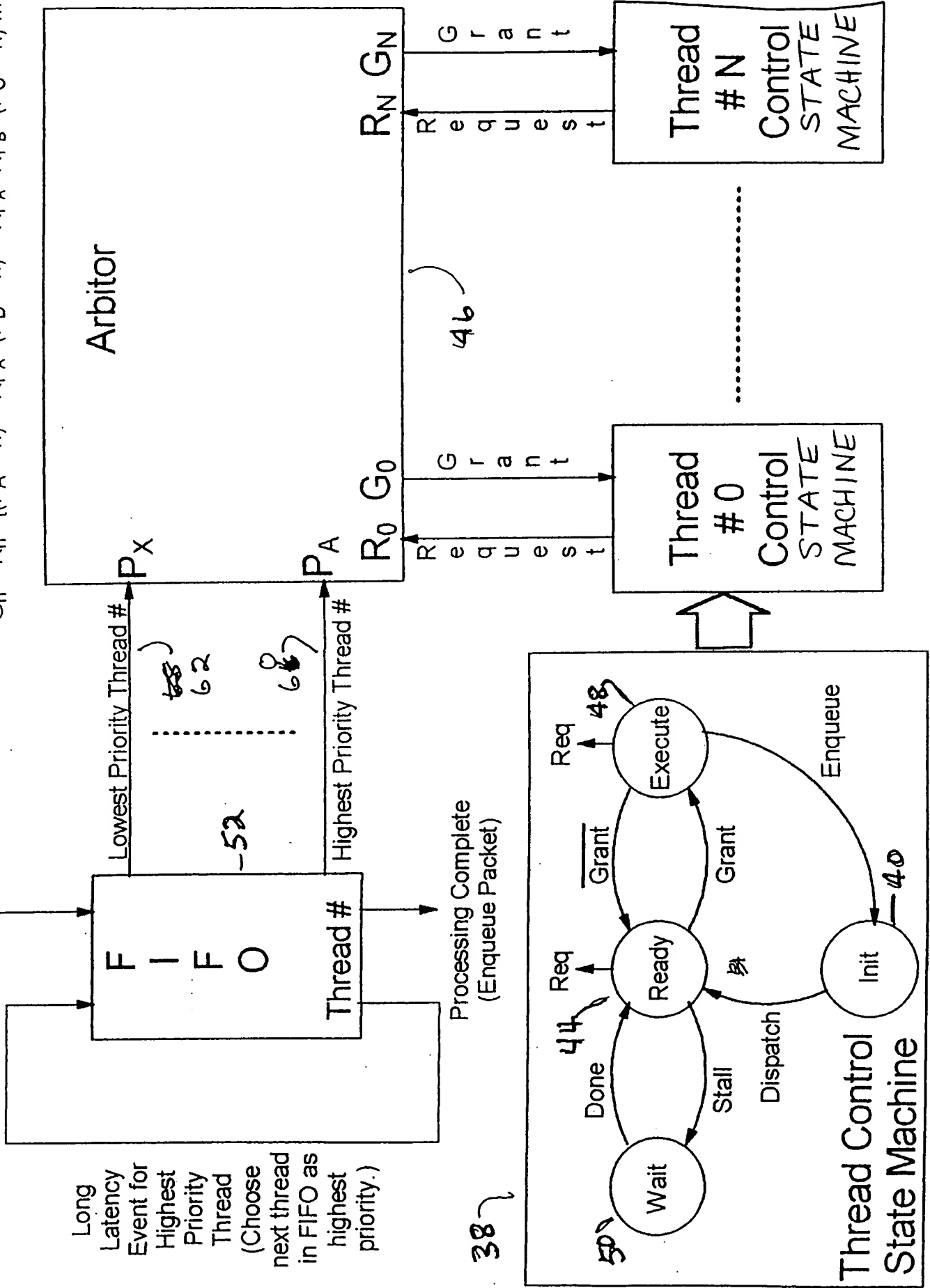


FIGURE 2 - Network Processor Configuration with Multiple Execution Threads

FIGURE 3 - Thread Execution Control

$$G_n = R_n \cdot \{(P_A = n) + \overline{R_{P_A}} \cdot (P_B = n) + \overline{R_{P_A}} \cdot \overline{R_{P_B}} \cdot (P_C = n) \dots$$



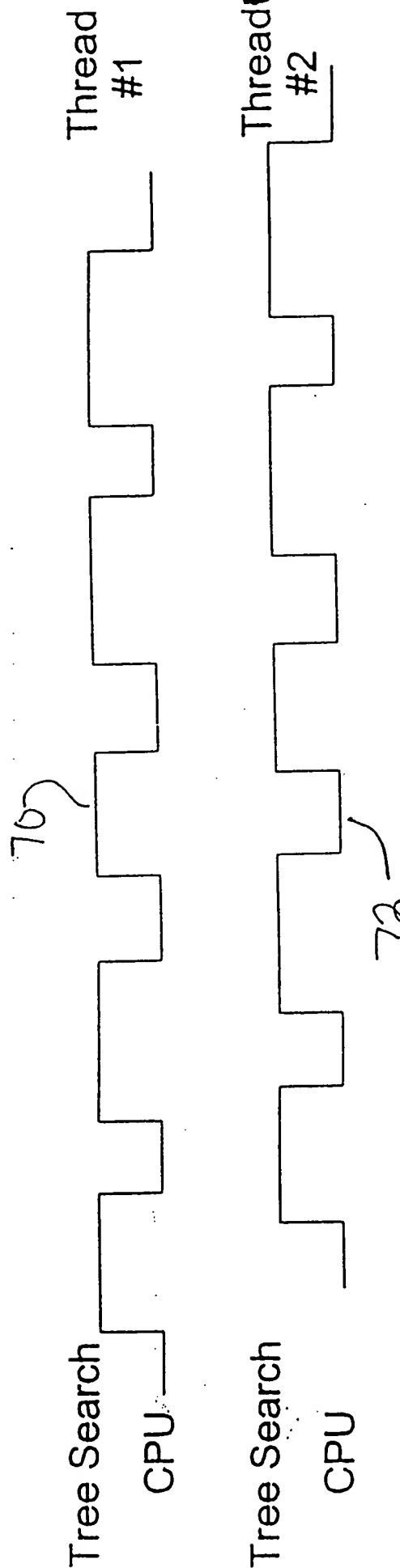


FIGURE 4 - Multiple Execution Threads